



Mr. Brett Hamilton is the Chief Engineer for Trusted Microelectronics in the Flight Systems Division of the Global Deterrence and Defense Department at Crane Division, Naval Surface Warfare Center, Crane, Indiana. Mr. Hamilton serves as the technical lead and subject matter expert on issues pertaining to microelectronics integrity of electronic components used in Department of Defense Weapon and Cyber Systems. His experience includes the development of advanced techniques for malicious and counterfeit circuit detection and security feature robustness evaluations. Brett holds multiple patents, and his expertise is nationally-recognized as he is routinely called upon to advise senior leadership regarding issues involving microelectronics trust and integrity issues. Brett currently serves as the Navy's technical representative supporting DASN (RDT&E) on developing a long-term mitigation strategy for insuring microelectronics integrity in the aftermath of the IBM foundry sale and as the DoD lead for the recently chartered Joint Federation Assurance Center (JFAC) for Hardware Assurance (HwA). Mr. Hamilton is also a key government team member on ONR, DARPA and IARPA programs involved in microelectronic integrity and trust.