

**Committee on Energy and Commerce**  
**U.S. House of Representatives**  
Witness Disclosure Requirement - "Truth in Testimony"  
Required by House Rule XI, Clause 2(g)(5)

1. Your Name: <i>Naimish Patel</i>		
2. Your Title: <i>CEO</i>		
3. The Entity(ies) You are Representing: <i>Gridco Systems</i>		
4. Are you testifying on behalf of the Federal, or a State or local government entity?	Yes	No <i>X</i>
5. Please list any Federal grants or contracts, or contracts or payments originating with a foreign government, that you or the entity(ies) you represent have received on or after January 1, 2013. Only grants, contracts, or payments related to the subject matter of the hearing must be listed.  <i>U.S. Army Research office grant, 5/8/2012</i>		
6. Please attach your curriculum vitae to your completed disclosure form.		

Signature: \_\_\_\_\_



Date: *2-27-2015*

# Naimish S. Patel

## PROFESSIONAL EXPERIENCE:

### September 2010 – Present

*Gridco Systems, Founder and Chief Executive Officer*  
*Woburn, MA*

Gridco Systems is a leader in Agile Grid Infrastructure solutions, enabling electric utilities to more effectively integrate renewable and distributed generation, increase energy efficiency, manage peak capacity, and improve system reliability. The Gridco Systems emPower™ Solution combines modular power electronics, advanced controls, distributed networking, and power system analytics to deliver the industry's only end-to-end hardware and software platform purpose built to solve utilities' current and emerging distribution challenges in a distributed, dynamic, and decoupled fashion.

### October, 2007 – August 2010

*General Catalyst Partners, Entrepreneur in Residence* *Cambridge, MA*

Investigating opportunities to create transformative businesses at the intersection of information technology with life sciences and clean energy.

### February 1998 – June 2007

*Sycamore Networks, Founder and Chief Technology Officer* *Chelmsford, MA*

Responsible for aligning Sycamore's technology and product strategy with the needs and requirements of global service providers deploying intelligent optical networks. As one of the four founding members of Sycamore, was responsible for the architecture and development of Sycamore's industry-recognized portfolio of optical transport and switching products. Responsible for Sycamore's IP portfolio. Represented Sycamore within industry standards bodies such as the IETF, OIF, and ITU. Frequent speaker at industry conferences. As part of the business development team, was responsible for assessing investment and acquisition opportunities.

### March, 1997 – February 1998

*Digital Equipment Corporation, Principle Architect* *Littleton, MA*

Led a team responsible for the architectural development of a next generation high-capacity IP router. Responsible for the design of arbitration and scheduling algorithms and their ASIC-based implementations.

### June, 1996 to March, 1997:

*MIT Lincoln Laboratories, Principle Investigator* *Bedford, MA*

In the Optical Communications Group, investigated information theoretic issues concerning the security and Shannon capacity of multi-access WDM optical networks employing erbium-doped fiber amplifiers. Continuing experimental work (from Master's thesis research) on devices for high speed optical networks.

**Summers of 1993, 1994, 1995:**

*MIT Lincoln Laboratories, Masters Thesis*

*Bedford, MA*

VI-A coop student in the Optical Communications Group. As part of a research effort on 100 Gb/s all-optical time division multiplexed (TDM) networks, conducted research for Master's Thesis entitled "High speed all-optical switching based on a single arm interferometer". Designed and implemented a novel semiconductor based all-optical switch and logic gate called an Ultrafast Nonlinear Interferometer (UNI). Demonstrated an all-optical demultiplexer, AND, NOT, XOR, OR, and NOR gate at 40 Gb/s based on the UNI. This work held the record for the highest rate demonstration of bit-wise all-optical logic. Also built a 160 Gb/s optical demultiplexer based on the UNI and began initial characterization towards the end of the research.

**PATENTS AND PUBLICATIONS:**

Holder of 21 US and International Patents, and author of numerous papers on the topics of high-speed optical devices, optical networking architectures, high-capacity TDM and packet switch design, and utility-scale power converters. Frequently invited to host panel discussions and speak on the subjects of networking and communications technologies, and transformation of the electric grid at major industry events.

**BOARD AFFILIATIONS:**

Board of Directors, Advanced Energy Economy

President's Council, Olin College

**EDUCATION:**

Massachusetts Institute of Technology, Cambridge, MA ('91-'96).

GPA: 4.9/5.0

Bachelor of Science Degree, June 1996

Master of Engineering Degree, June 1996

Recipient of David Adler Award for best Master's thesis in Electrical Engineering.

Electrical Engineers Degree, December 1996